ABSTRACT

A data access system for accessing data stored in a first and a second memory devices. The first and second memory devices have a difference of latency ΔL that constitutes a time-duration by which the first memory device starts an initial data access earlier than in the second memory device. A data access controller is implemented to simultaneously access data in the first and second memory devices and to stop accessing data in the first memory device once a data access operation has begun in the second memory device. Therefore, the first memory device stored data only accessed initially in a time duration corresponding substantially to the difference of latency ΔL before the data access operations is started in the second memory device.